


**APPLICATION DATA SHEET**

Electronic Version v14

Stylesheet Version v14.0

<b>Title of Invention</b>	A MANUFACTURABLE METHOD AND STRUCTURE FOR DOUBLE SPACER CMOS WITH OPTIMIZED NFET/PFET PERFORMANCE		
Application Type :		regular, utility	
Attorney Docket Number :		FIS920030397US1	
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as our attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.

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